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# BIOSTAR GROUP

## IG41B-M7S

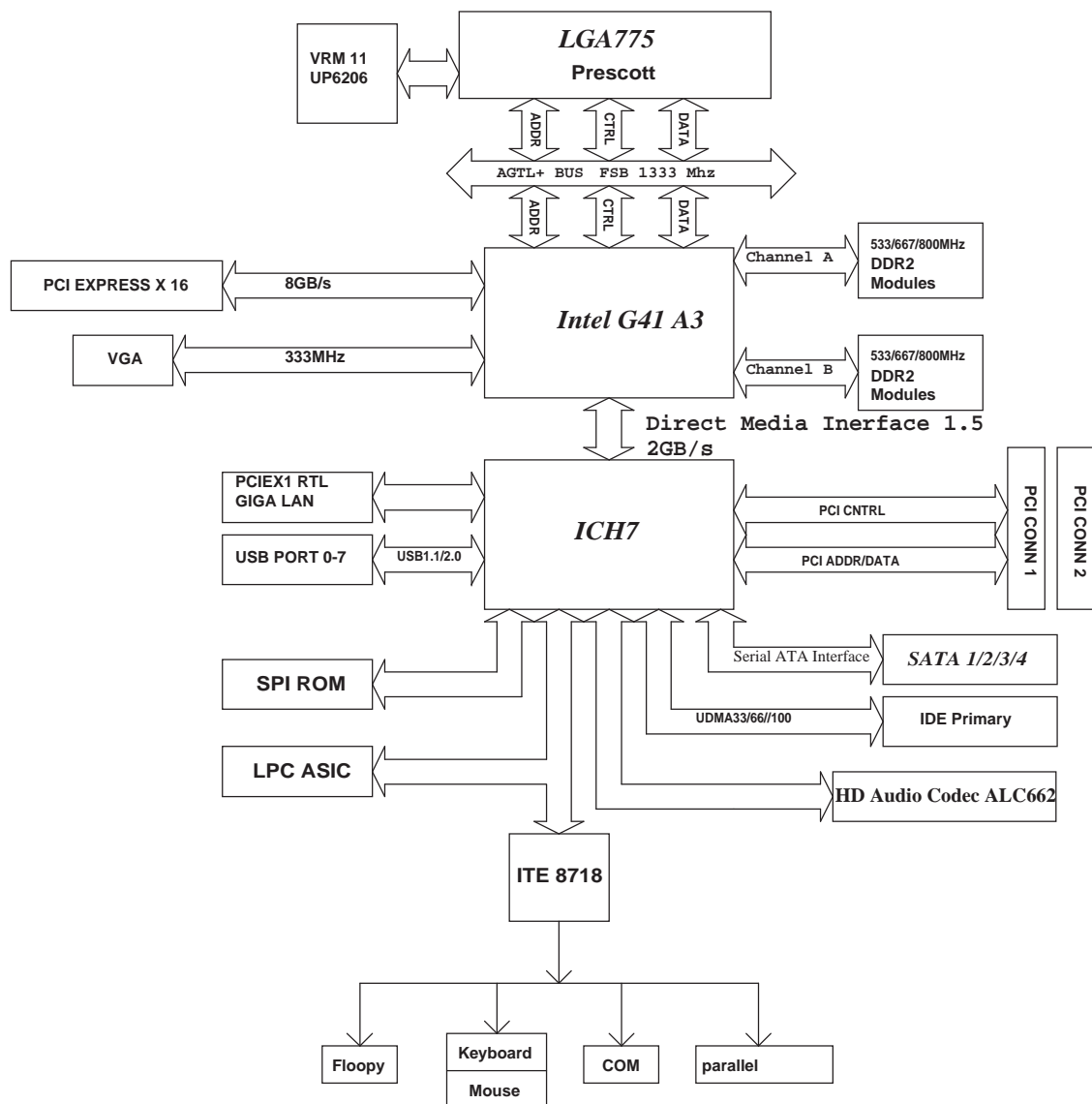
uATX


VER:6.0

775 CPU , FSB1333 ,  
PCI-Ex16 , DDR-II\* 2 ,  
1000 LAN,PCI\*2




Title			<b>Cover Sheet</b>		
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**BLOCK DIAGRAM**

 <b>映泰股份有限公司</b> <b>BIOSSTAR GROUP</b>			
<b>Block Diagram</b>			
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PCI Slot	IDE SEL	INT#	GNT#	REQ#	CLOCK
1					
2					

Chips	IDE SEL	INT#	GNT#	REQ#	CLOCK	GPIO
PATA						
ICH7						
S						
I/O BIOS						
LAN						
ANTI-POP						
DIMM-OV						
CPU-OV						
SATA-LED						
S						
I/O						


 <b>映泰股份有限公司</b> <b>BIOS STAR GROUP</b>			
Title			
<b>General Spec.</b>			
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1. 載網MOS
  2. PAGE31 載網轉消盡綠乙賤S3逼停修抵
  3. PAGE32 蔚SIO 腔JP防痛效嶺
  4. PAGE27 28 蔚跪踪PORT腔措蘭莖鄭載網略75種躁乙 室狂嶺22K
  5. 党網PART REF NO.
  6. 2JVGA1.6, JVGA1.7, JVGA1.8, Q90.2, Q89.2, C529.2, C528.2, C530.2, C527.2, C524.2, C526.2, C525.2 網諱GND( 党網 貨? )  
C164 = 102pF  
FB10, AR2, FB13 = 0 ohm R
  7. WAKE#防痛1K效嶺網略4.7K效嶺
  8. BIOS ROM套4M網略8M
  9. LAN CONN 1容10PIN哨岷效嶺,莖 善華乙 岷1PIN莖 善華乙10PIN嶺華 (
  10. RTM876-665 SSOP 56P VTT\_PG略岷莖 紐此乙氣樓VCCP糾微玲踪3904諷菜盡綠,
  11. CPU FAN 腔12V莖 網略100UF
  12. Q28 D憤樓玲1000UF莖 乙PC23網略CPU\_GTLREFO腔? 乙弁禹祥曹
  13. 靖樓CLEAR CMOS險盡綠
  14. 載網NB PLL腔1.1V莖抑乙斯玲踪2300MOS等黃蠟1.1V鼎PLL斬
  15. 載網RESET盡綠
- 6.0 TO 6.1
1. 靖樓PSI 諷菜

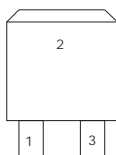
IG41B-M7S V6.0:

2009.05.19 LM324盡做莖埭埭MOS G憤乙S憤昭潔樓玲1K莖鄭乙賤樞莖埭坏堤祥佃隅玲搥喘疏修抵 (

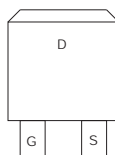
靖樓PC36

		<b>映泰股份有限公司</b> <b>BIOSSTAR GROUP</b>	
<b>Change List</b>		Rev 6.0	
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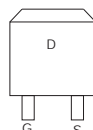
22U/25DE	5*7 mm
100U/16DE	6.3*11 mm
220U/10DE	6.3*11 mm
470U/16DE	8*11 mm
1000U/10DE	8*14 mm
1500U/16DE	10*25 mm
3300U/25DE	10*25 mm



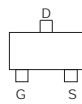
TO-263  
B55QS03



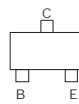
TO-263  
2SK3296



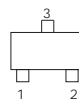
TO-252  
2ON03  
TM3055TL-S  
45N03  
FDD6030L



SOT-23  
2N7002  
SI2303S  
SI2301S



SOT-23  
2N3904  
2N3906  
MMBT2907A



SOT-23  
BAT54C  
BAT54S




T0-92  
LM431  
78L05-D  
LM432



T0-92  
2N2222A  
2N2097A



T0-92  
HSD882-D



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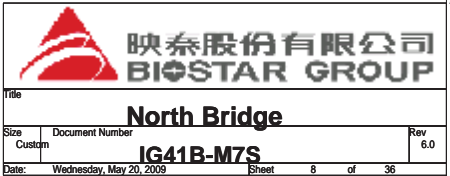
**Component Size**

IG41B-M7S

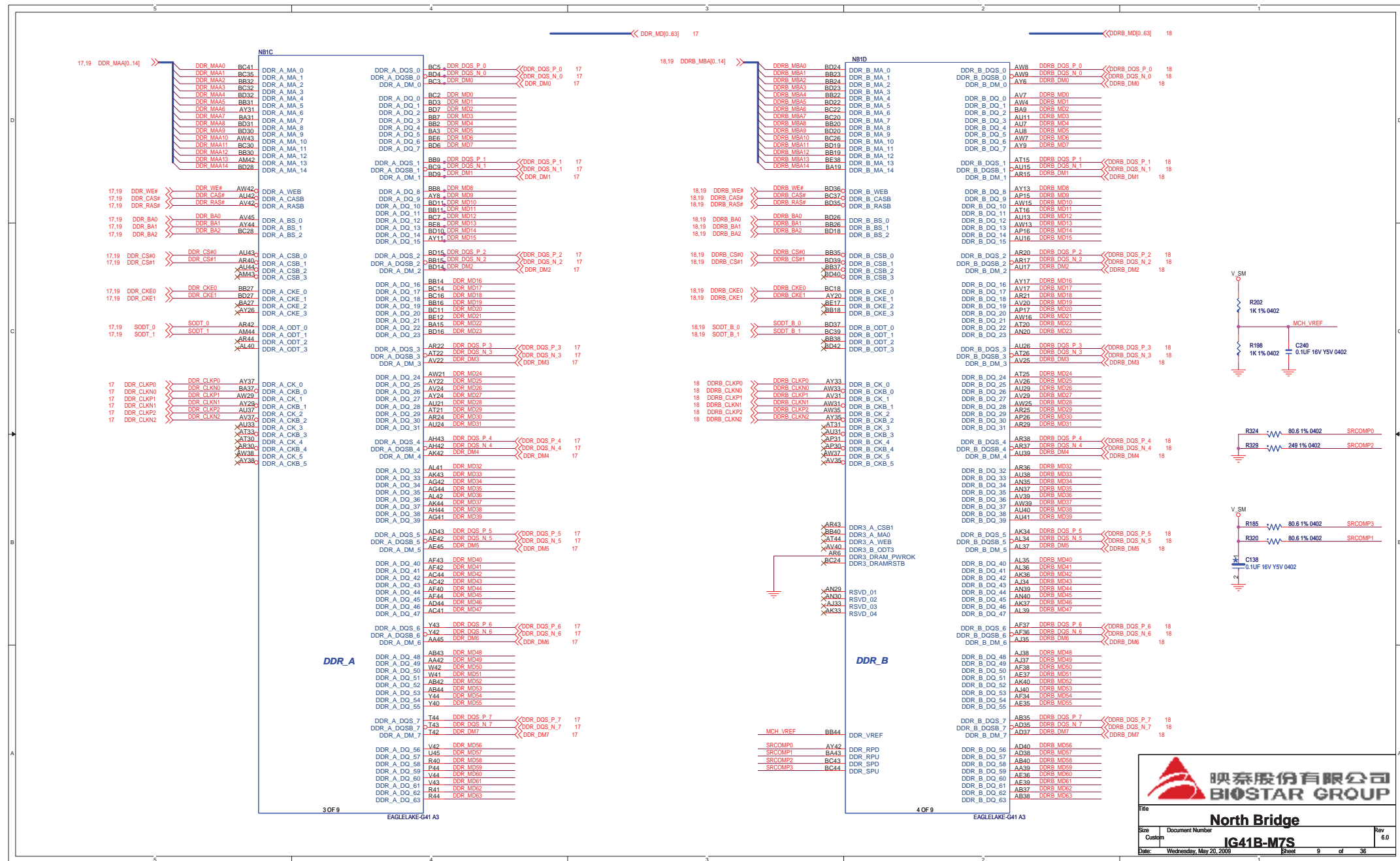
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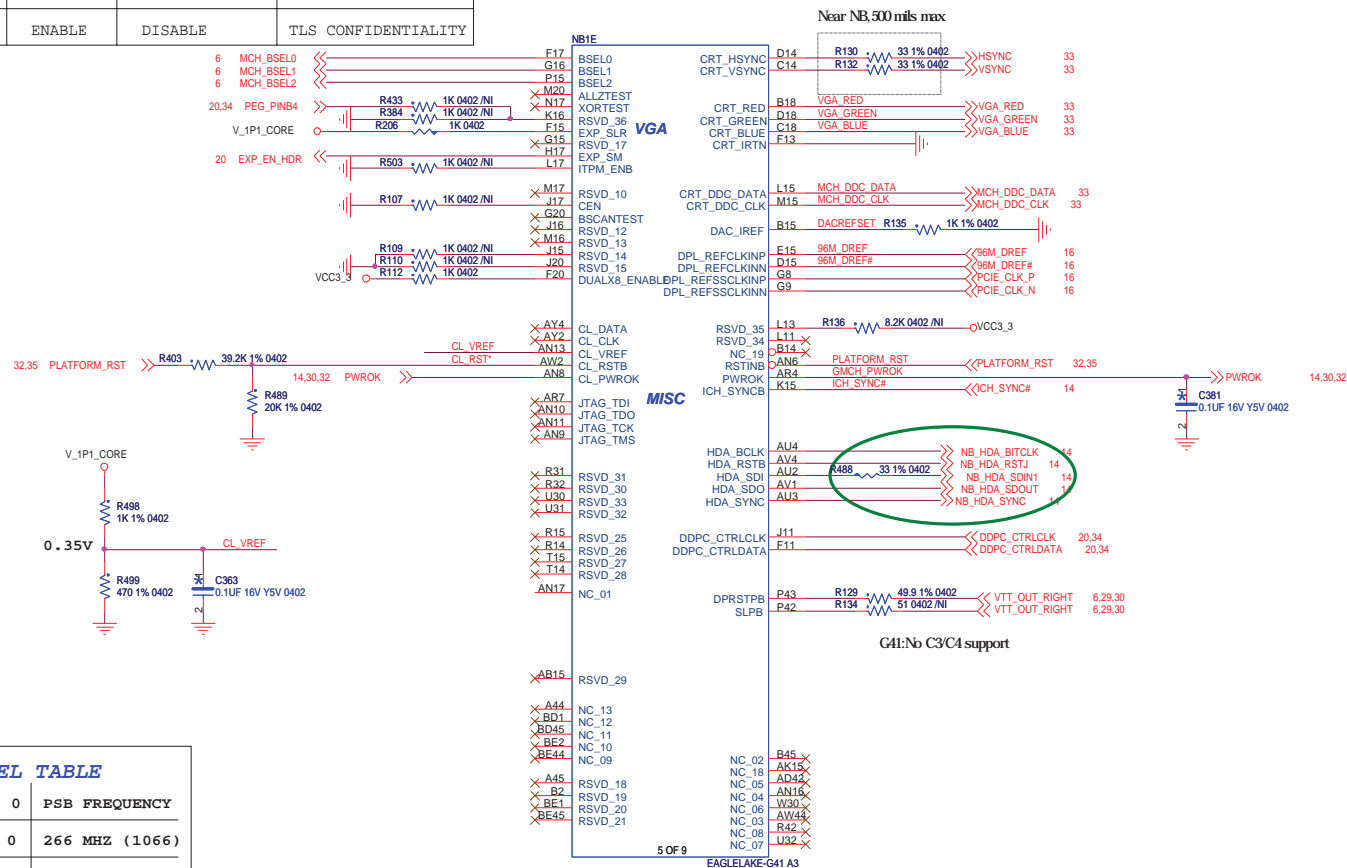






GMCH STRAPS TABLE

PIN	H	L	DESCRIPTION
MTYPE	DDR2	DDR3	MEMORY TYPE
EXP_SLR	NORMAL	RESERVE	PCI EXPRESS STATIC LANE REVERSAL
EXP_EN	CONCURRENT	NON-CONCURRENT	PCI EXPRESS / SDVO CO-EXISTENCE
TCEN	ENABLE	DISABLE	TLS CONFIDENTIALITY

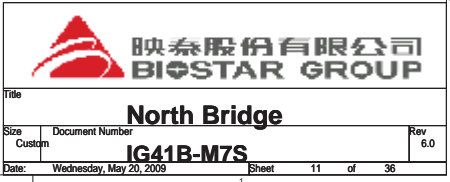


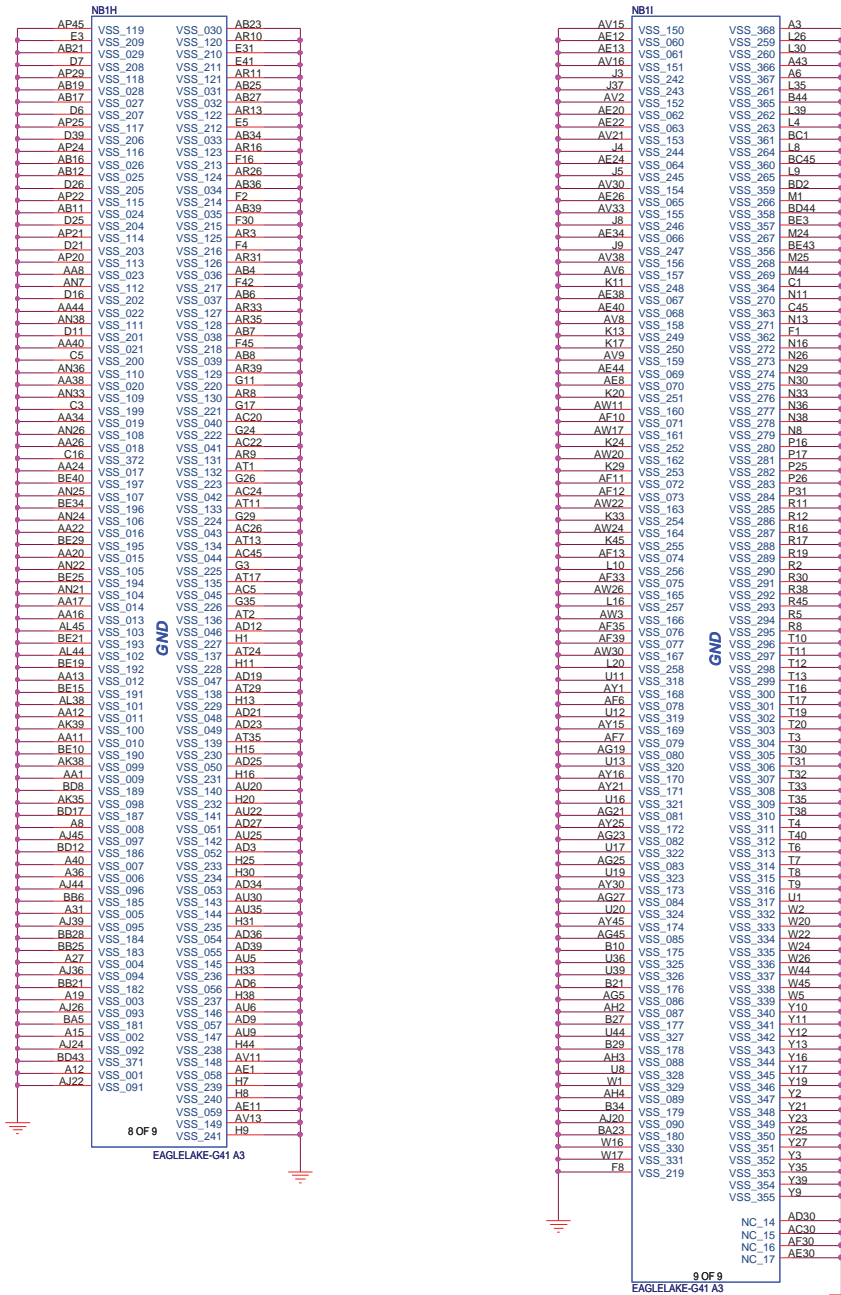
BSEL TABLE

2	1	0	PSB FREQUENCY
0	0	0	266 MHZ (1066)
0	0	1	133 MHZ (533)
0	1	0	200 MHZ (800)
1	0	0	333 MHZ (1333)



Title	NB(4)		
Size	Custom	Document Number	IG41B-M7S
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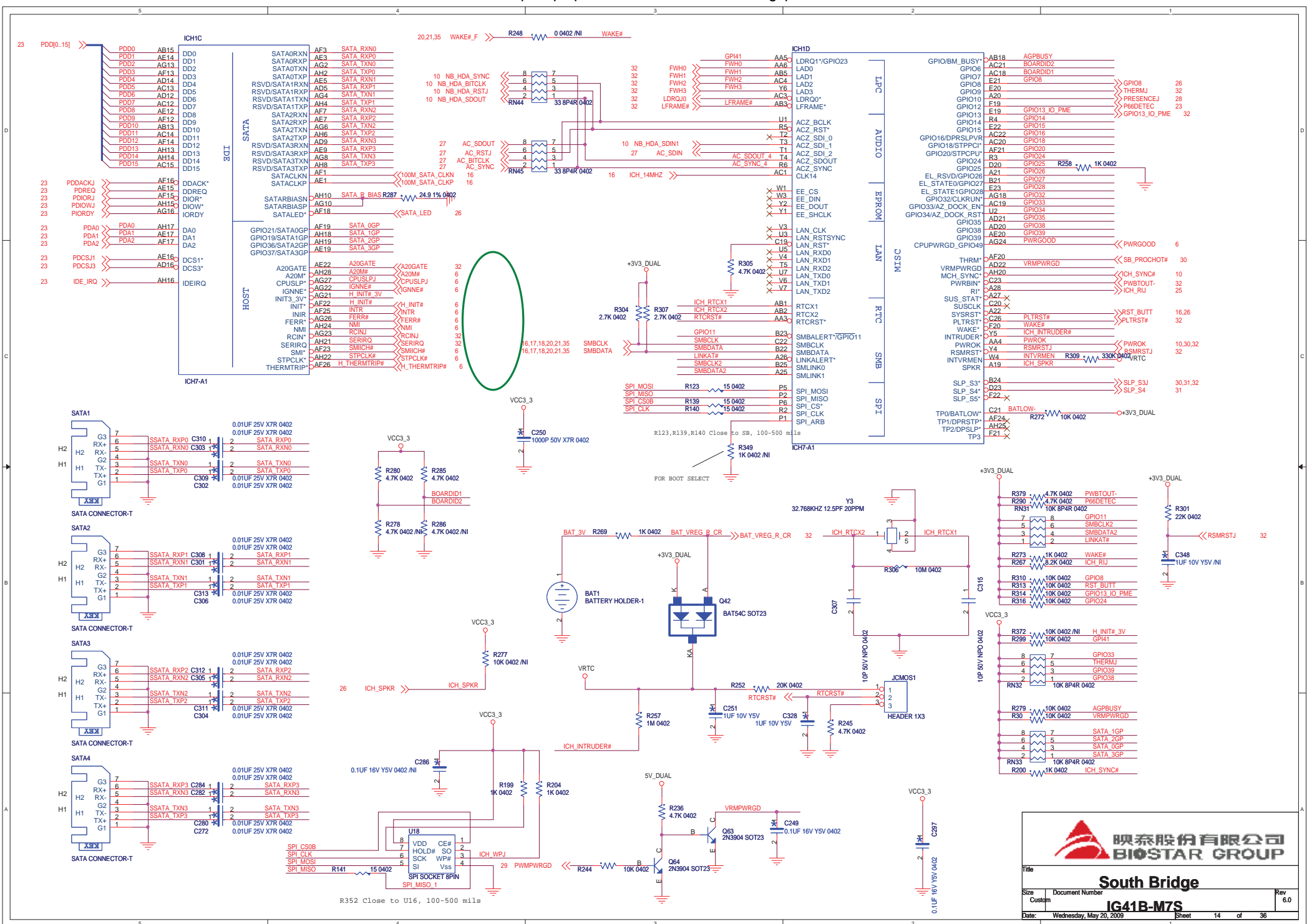
映泰股份有限公司  
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Title: **NB(5)**

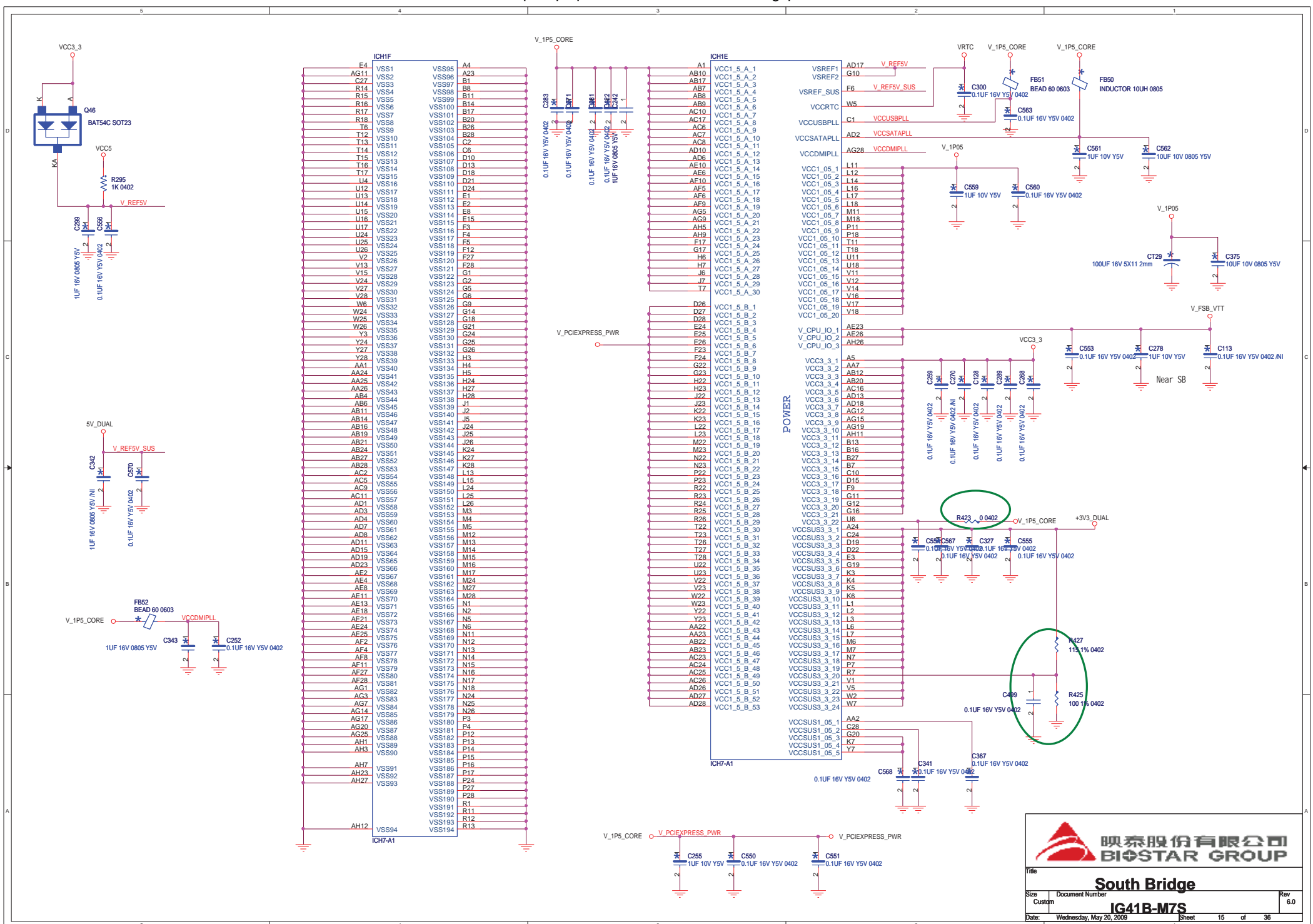
Size: Custom Document Number: **IG41B-M7S**

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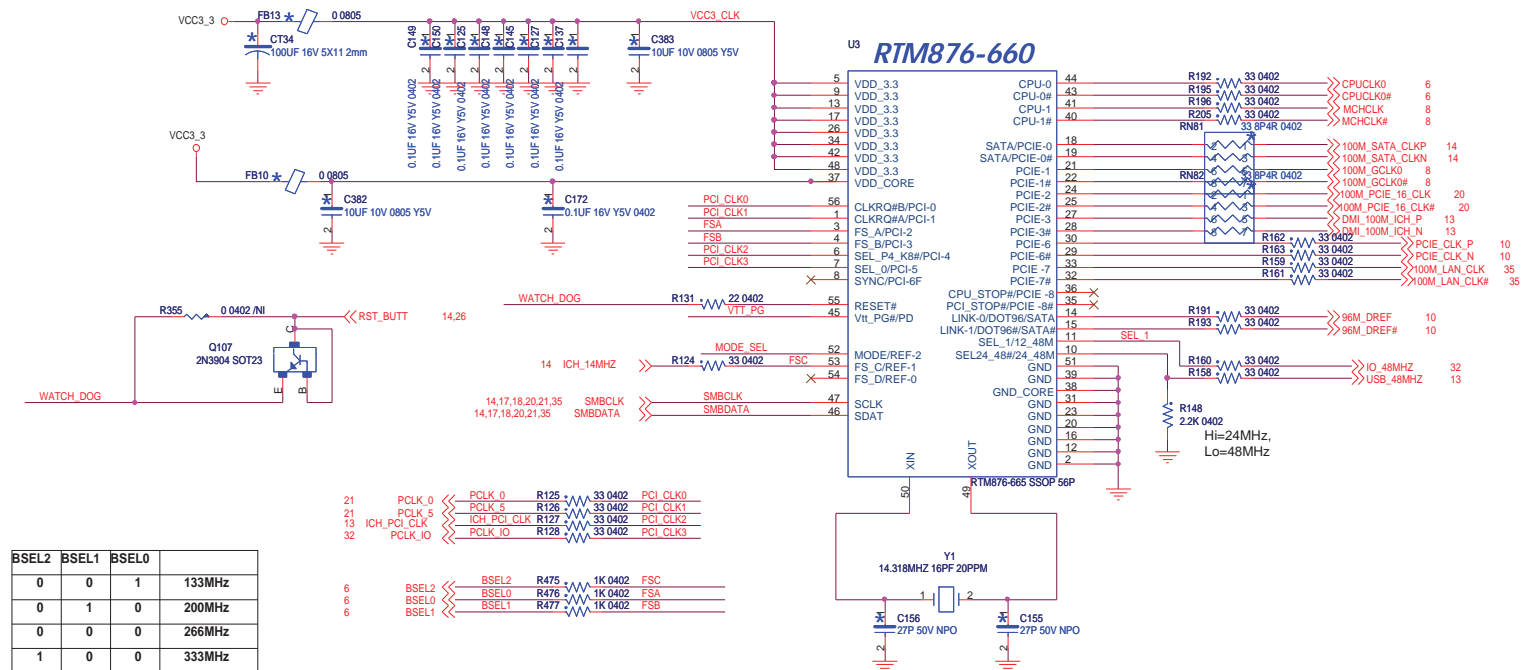
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**South Bridge**  
**IG41B-M7S**

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Rev 6.0



VCC3\_CLK

R486 1K 0402 NI PCI\_CLK2  
R493 1K 0402 NI PCI\_CLK3  
R494 1K 0402 NI MODE\_SEL  
R495 1K 0402 NI SEL\_1

PIN6 PCIF\_CLK1=SEL\_P4/K8(internal pull high)  
Hi = P4 0.8V push-pull CPU clock  
Lo = K8 3.3V push-pull CPU clock

PIN52 MOD\_SEL=SEL\_PIN35/36=PCIE-8(internal pull low)  
MODE=0 PIN33/34 is PCIE8#/PCIE8  
MODE=1 PIN33/34 is PCI\_STOP#/CPU\_STOP#

Pin11 Sel-1	Pin7 Sel-0	Pin14/15 Link/DOT/SATA	Pin18/19 SATA/PCIE	Pin12-48M	Chipset
0	0	Link	SATA	12M	Sis
0	1	DOT	SATA	48M	Intel W/GFX
1	0	Link	PCIEX	48M	VIA
1	1	SATA	PCIEX	48M	Intel

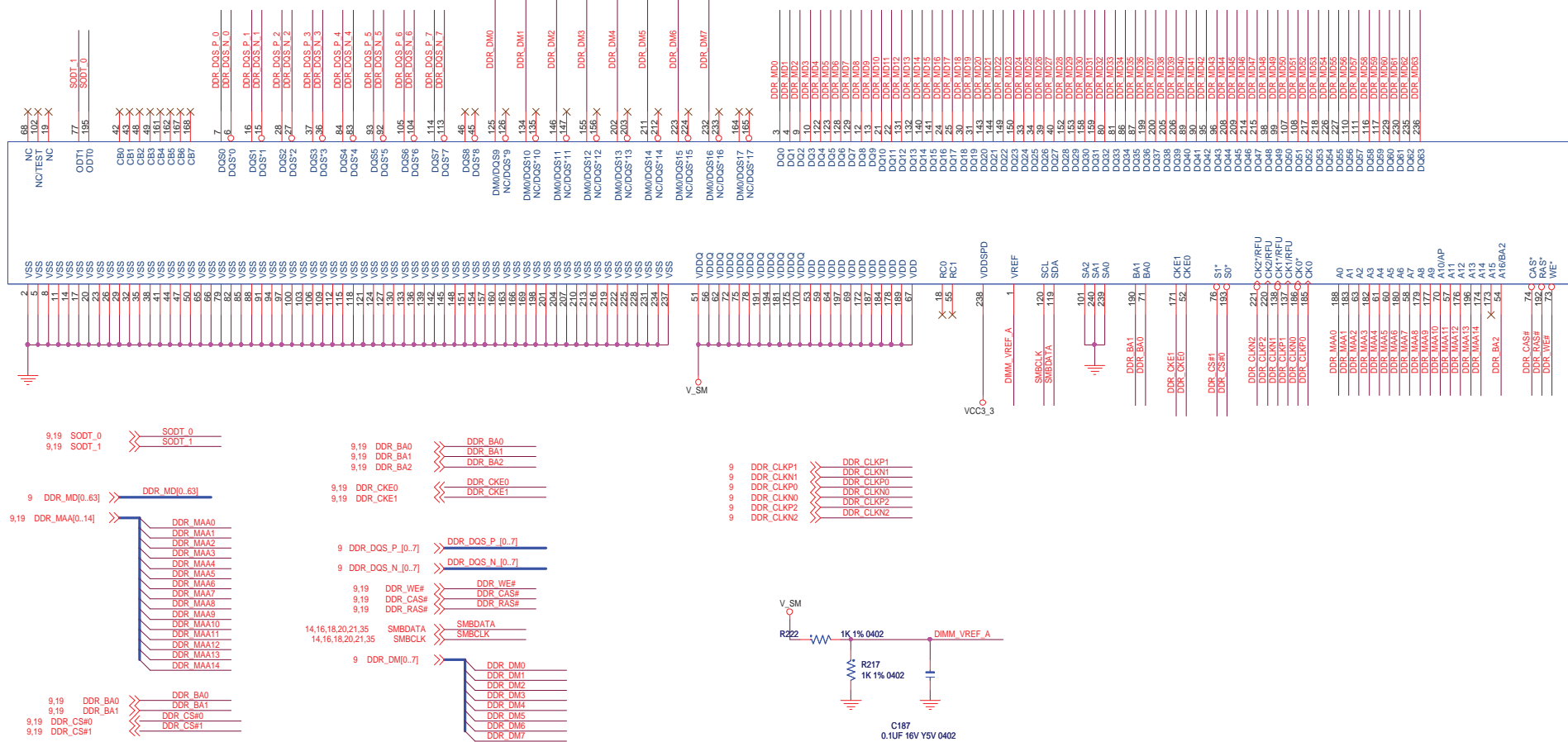
For Intel Chipset w/o GFX (SEL-1/0 = 11)  
DOT96 is not necessary

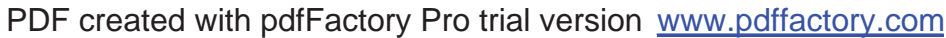

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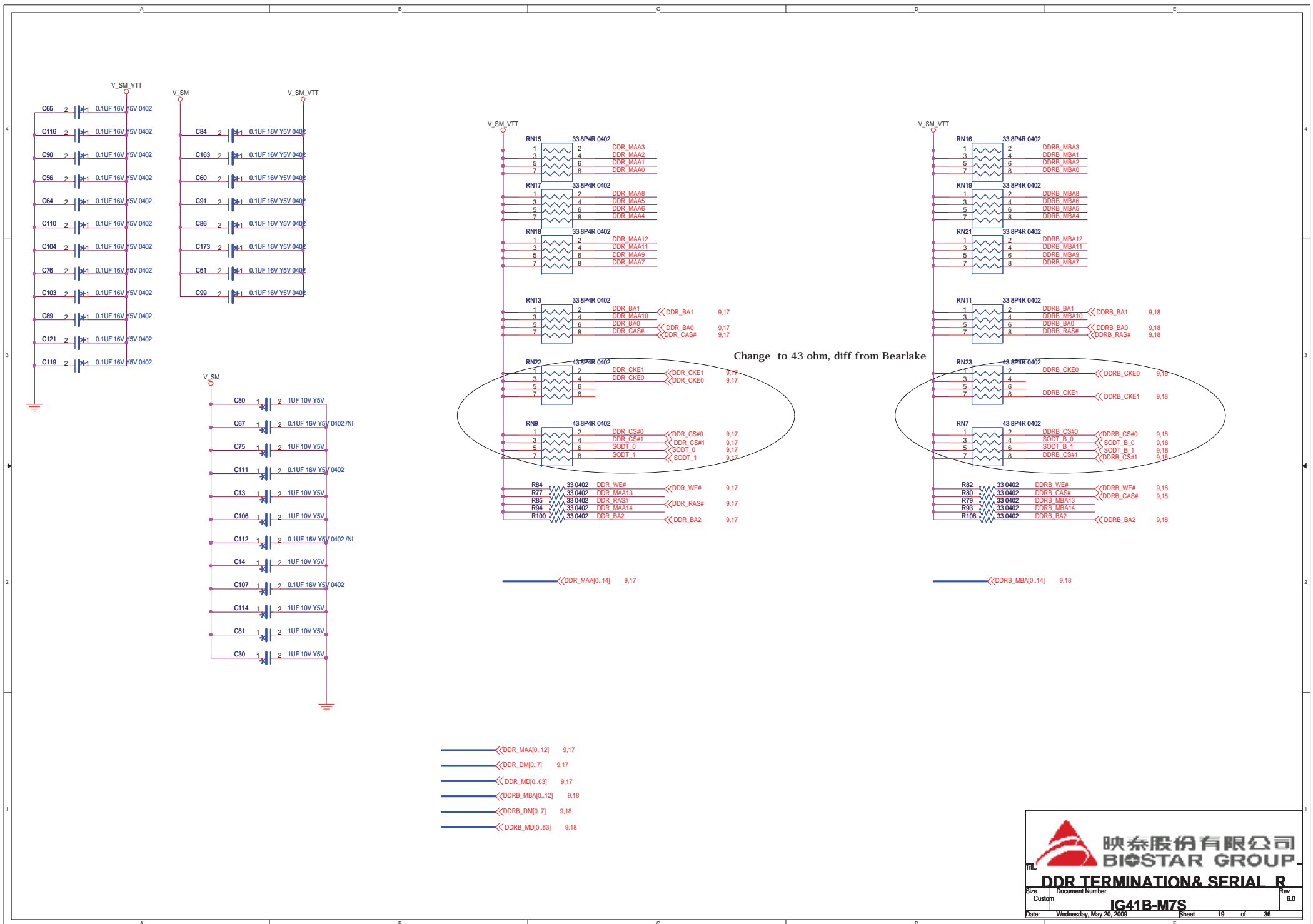
Title		<b>CLK GEN / BIOS (FWH)</b>	
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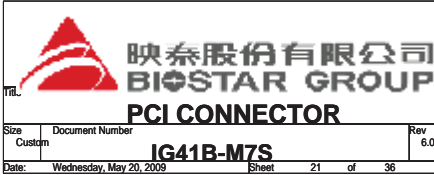


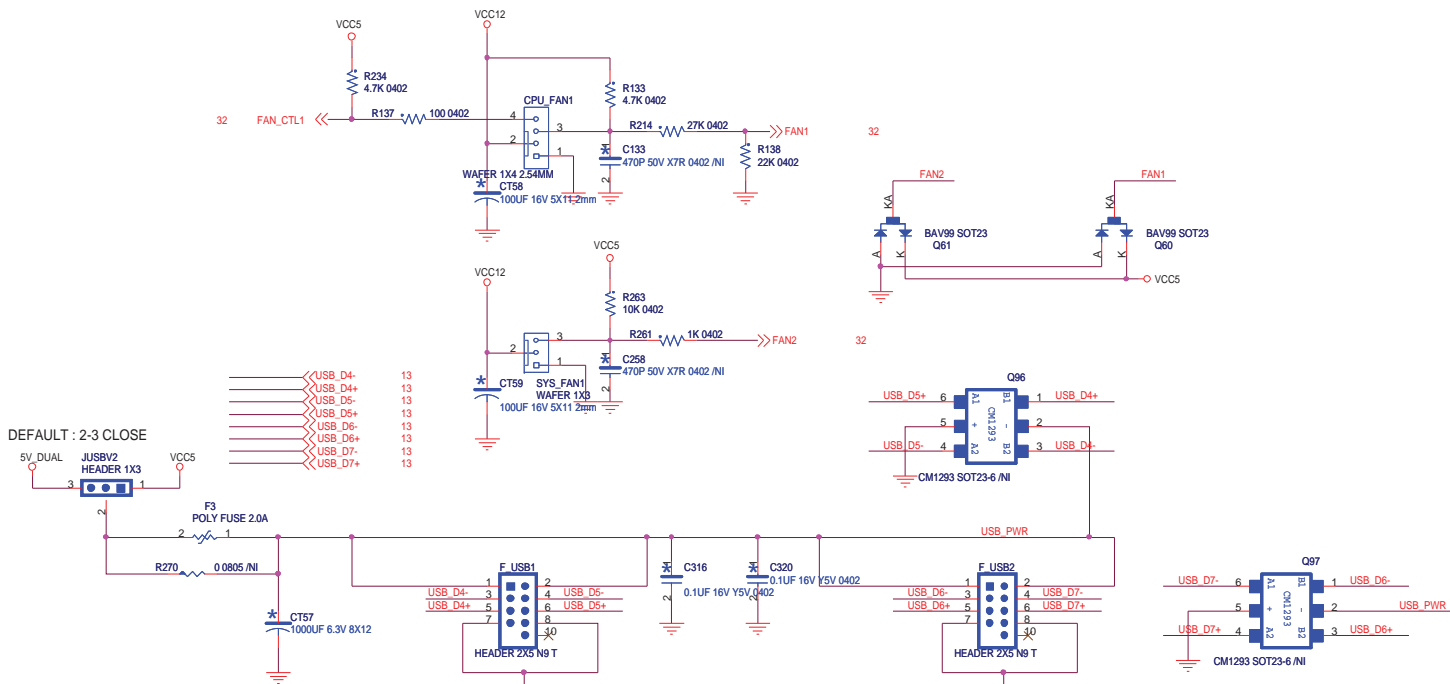
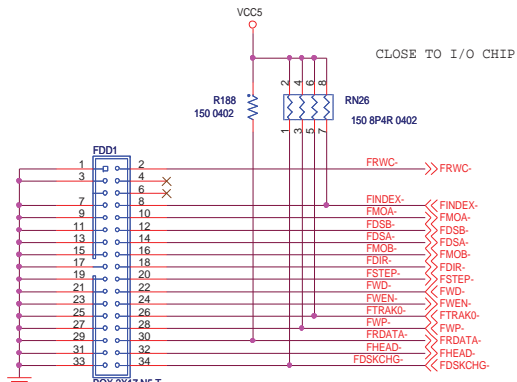


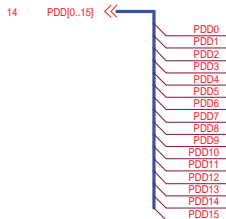


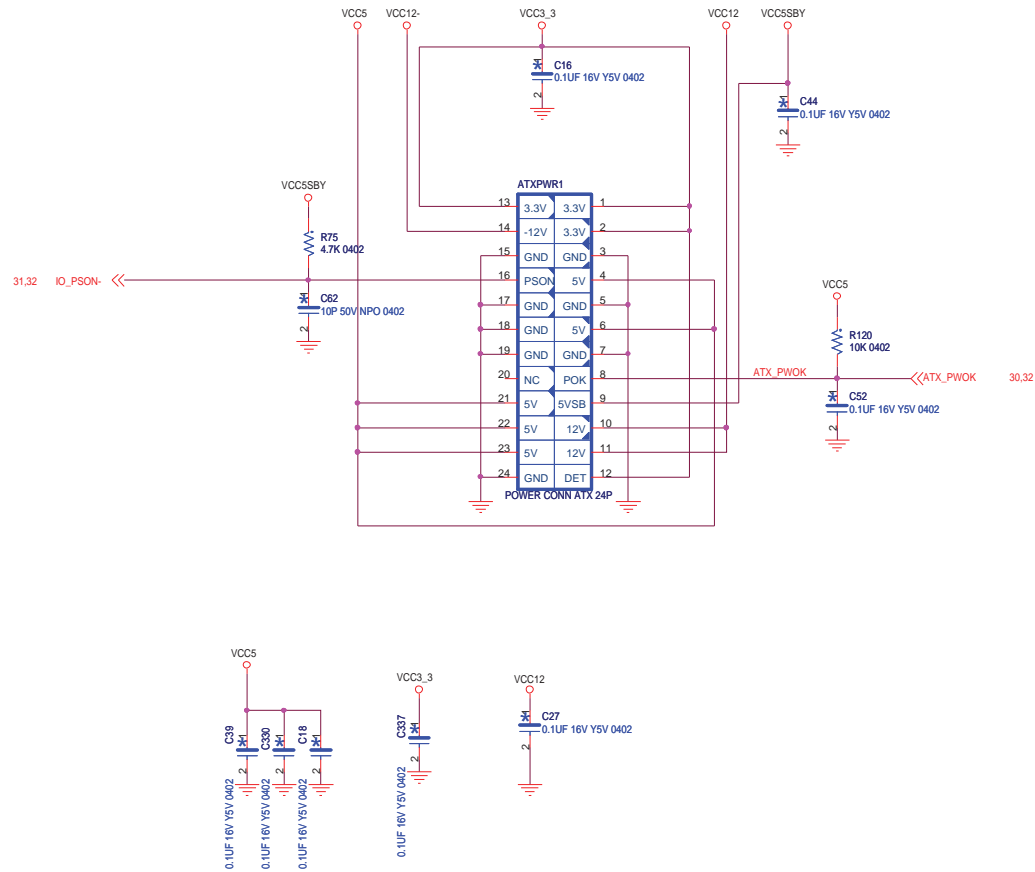







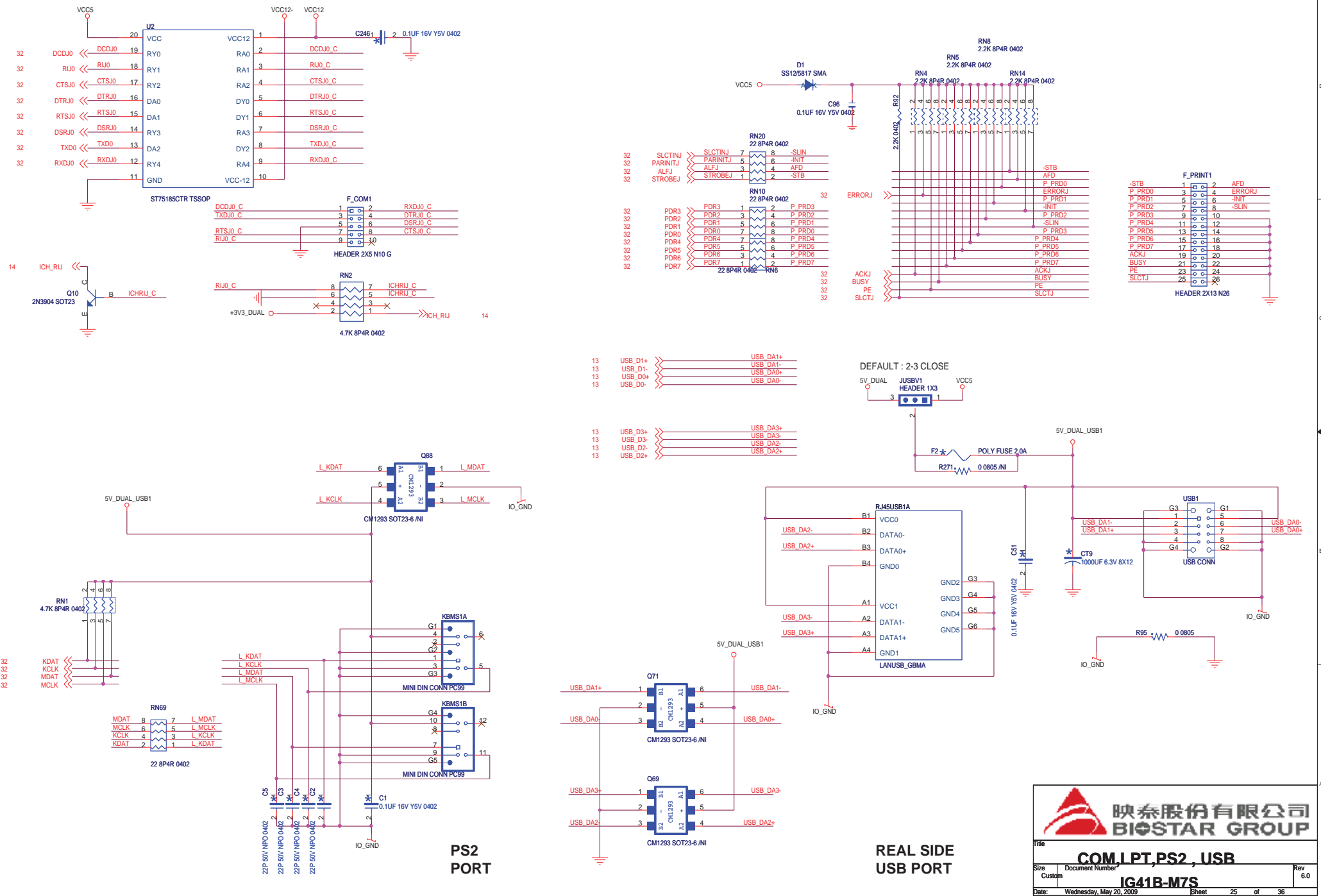


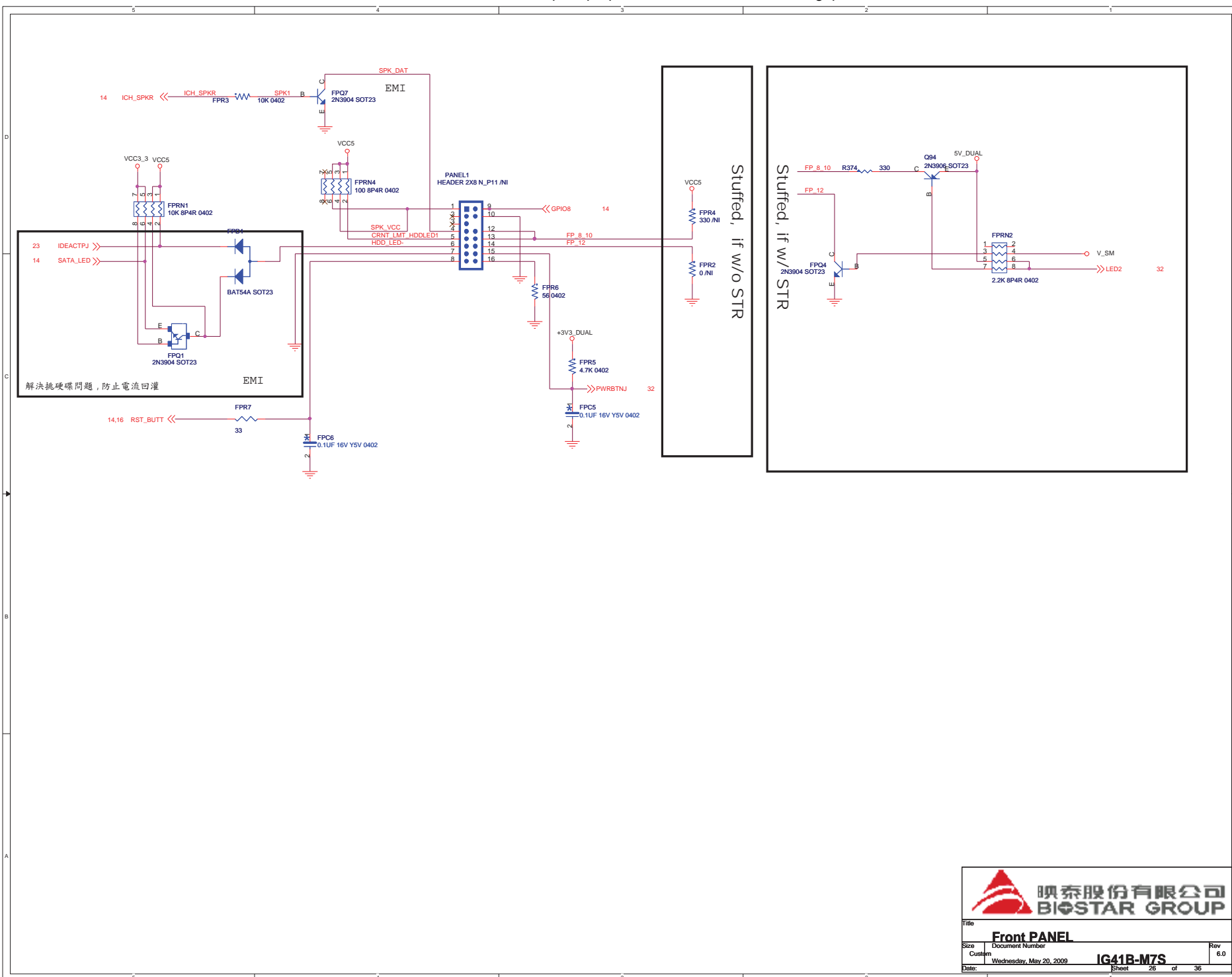




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<b>ATX POWER &amp; Bypass CAP</b>	
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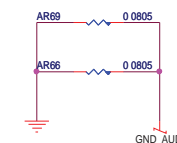
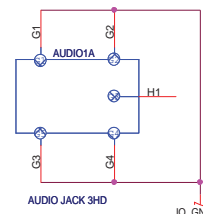
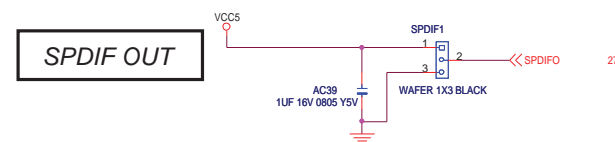
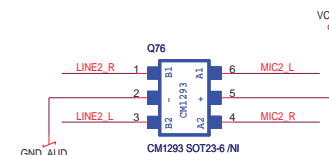
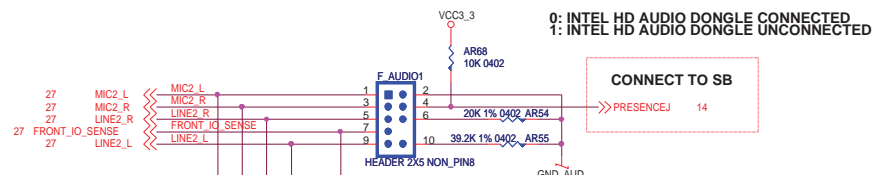
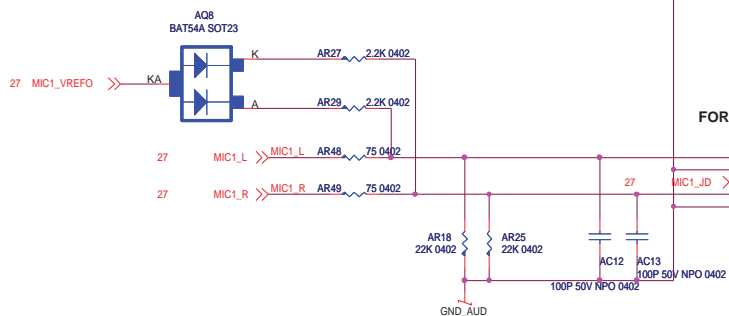
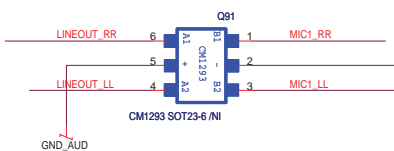
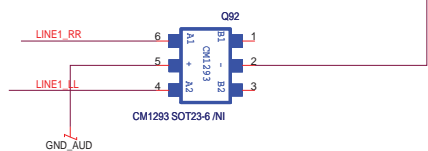
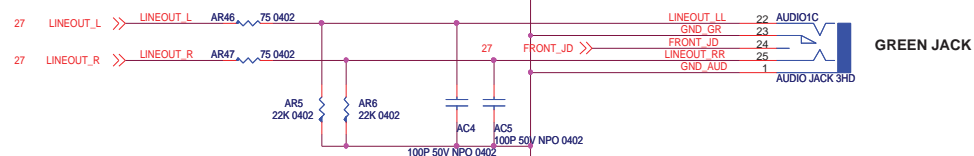
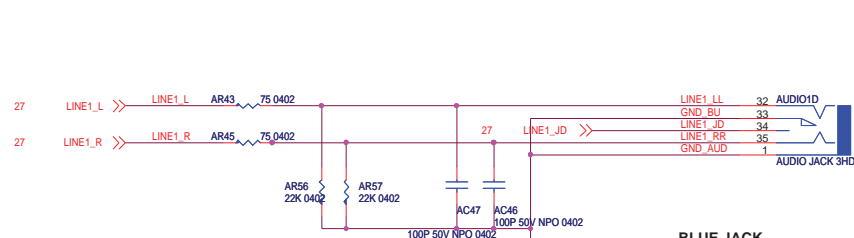








# Rear Panel Onboard Analog I/O



**映泰股份有限公司**  
**BIostar GROUP**

Title: **audio connector**

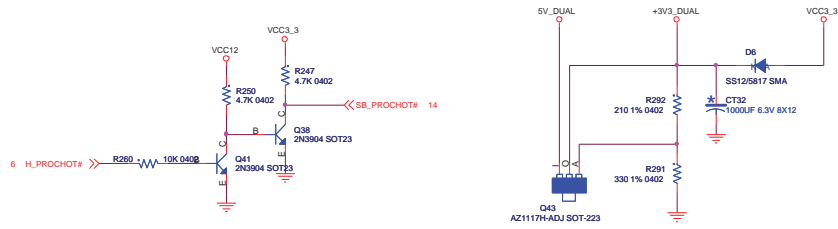
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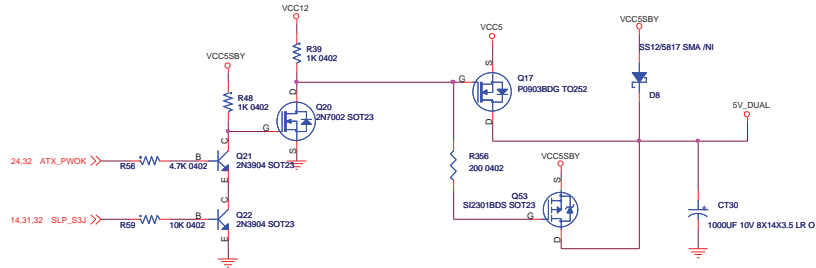
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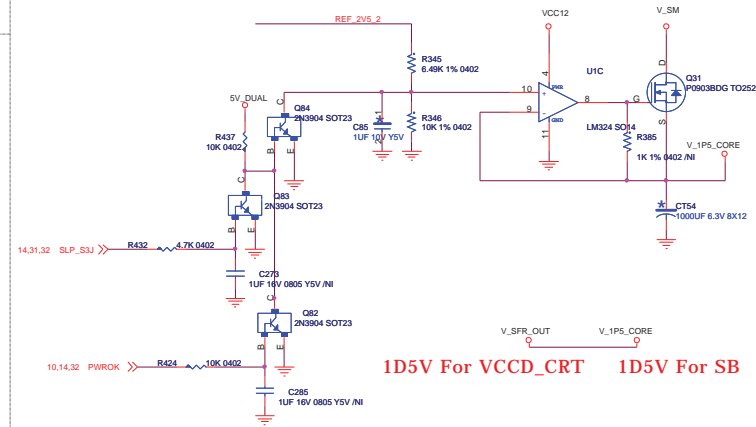
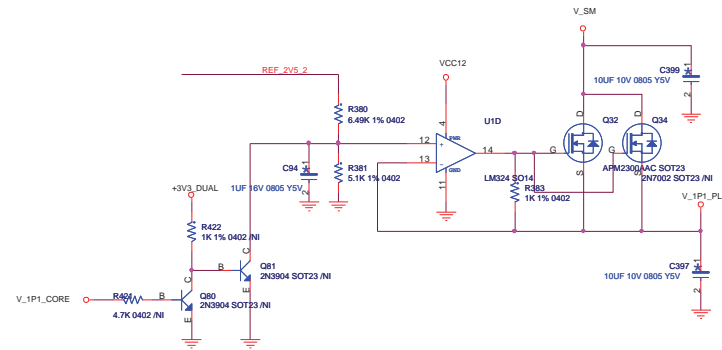
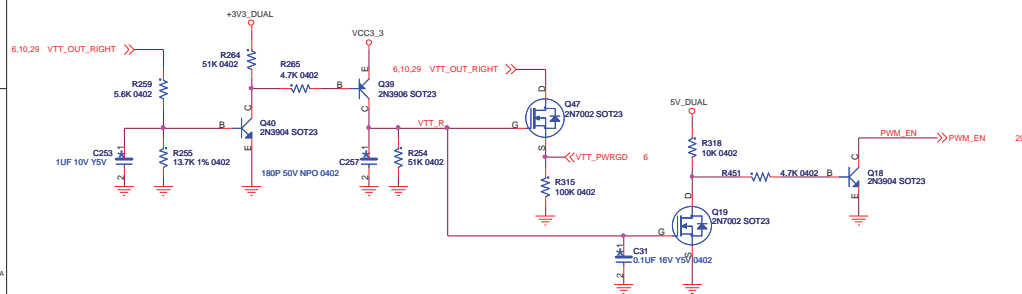
### +3V3\_DUAL



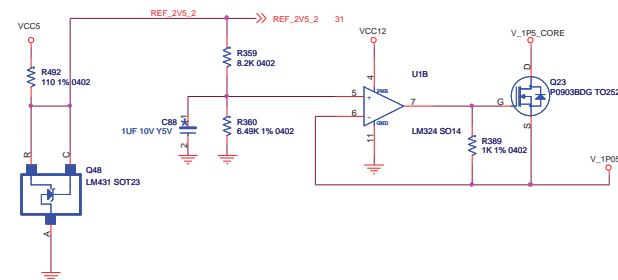
### 5V\_DUAL



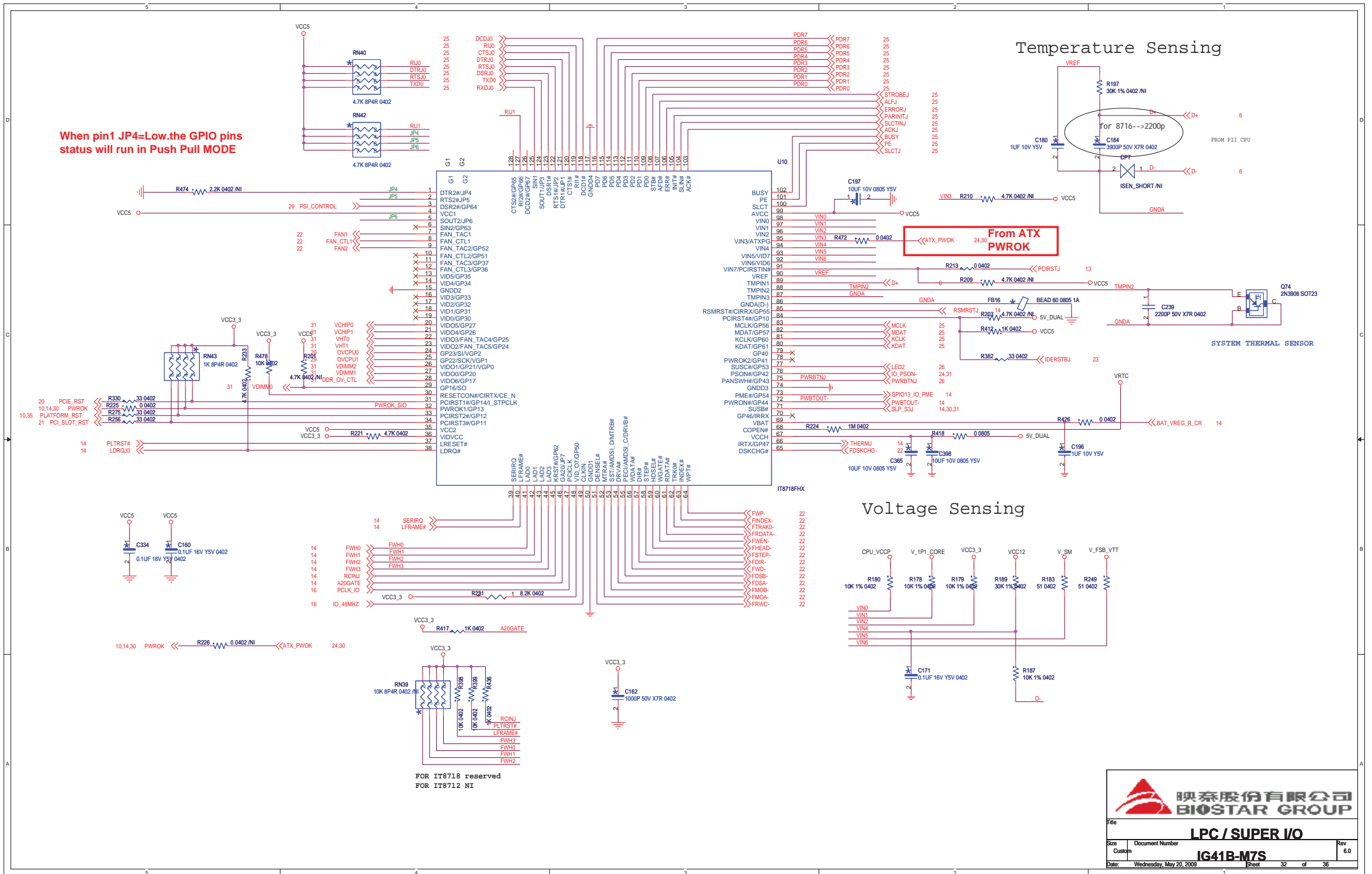
### PWM Sequence



1D5V For VCCD\_CRT 1D5V For SB



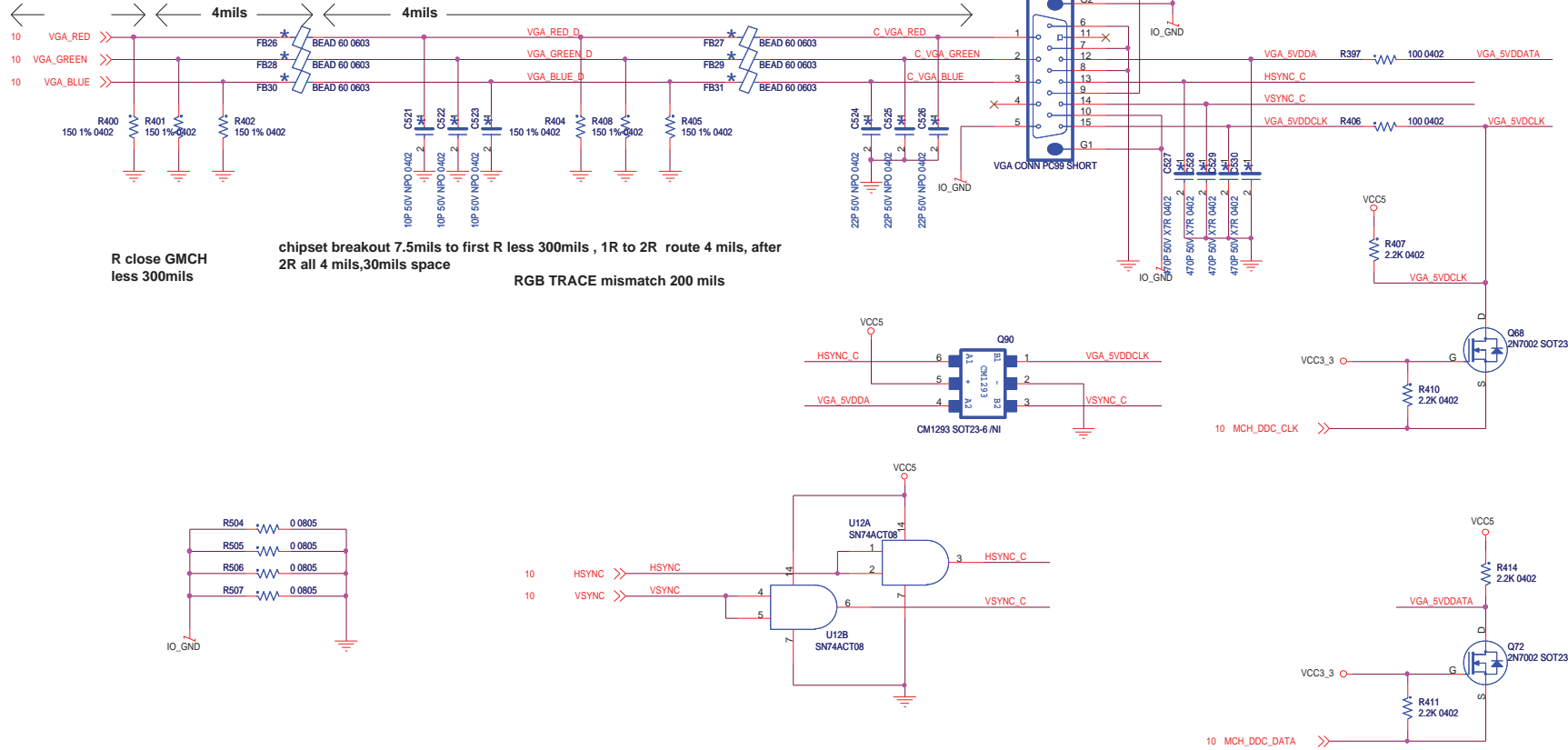






close to GMCH

W=7.5mils,S=6mils



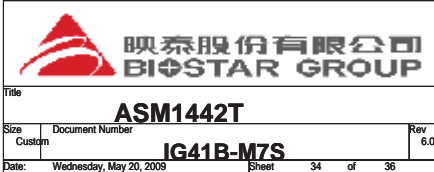
**映泰股份有限公司**  
**BIOSTAR GROUP**

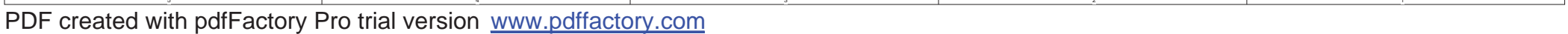
**VGA CRT CONNECTOR**

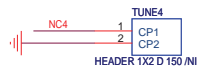
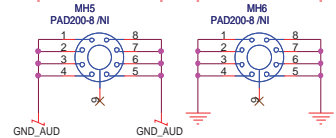
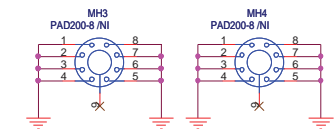
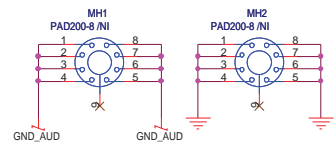
**IG41B-M7S**

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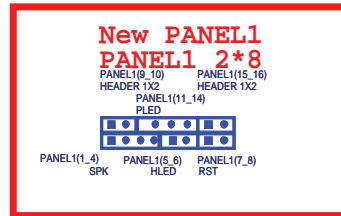
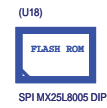
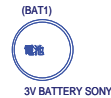
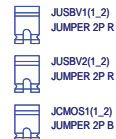
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Impedance Testing Coupon





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Title <b>BOM &amp; MH &amp; Coupon</b>		
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